

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

3,375,252,408	H. Bohlen, U. Behringer, W. Kulcke, and P. Nehmiz	Electron Beam Projection Lithography
3,375,266,408	D. E. Staiger	Method of and Arrangement for Generating Pulses of an Arbitrary Time Relation During Immediately Successive Assumed Pulses
3,468,997,408	K. Hartmann and H. Matthaei	Electromagnetic RAM Actuator with Clapper Element, in Particular for Impact Printers

France

134,374	M. Cukier	Phase-Locked Clock
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United Kingdom

90,104	C. Smith and T. G. Cole	Dual Polarity Switchable Operational Amplifier Circuit
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United States

4,611,203	T. N. Criscimagna, H. S. Hoffman, Jr., and W. R. Knecht	Video Mode Plasma Display
4,673,987	K. Toyokawa	Compressed Block Code for Facsimile Transmission
4,688,073	G. R. Goth and S. D. Malaviya	Lateral Device Structures Using Self-Aligned Fabrication Techniques
4,697,923	G. W. Jones, W. R. Pratt, and W. J. Summa	Method for Visual Inspection of Multilayer Printed Circuit Boards
4,712,216	R. J. Glaise	Method and Device for Correcting Errors in Memories
4,712,916	J. B. Gunn	Apparatus for Inspection of the Walls of Deep Holes of Minute Diameter
4,713,140	Z.-J. Tien	Laser Luminescence Monitor for Material Thickness
4,713,708	M. T. Krounbi and O. Voegeli	Magnetoresistive Read Transducer
4,713,711	R. E. Jones, Jr., R. E. Lee, and T.-H. Yeh	Thin Film Magnetic Transducer Having Center Tapped Winding
4,713,751	P. F. Dutton and E. W. Jackson, Jr.	Masking Commands for a Second Processor When a First Processor Requires a Flushing Operation in a Multiprocessor System
4,713,773	J. F. Cooper, E. S. Kirkpatrick, and R. Linsker	Method for Distributing Wire Load in a Multilayer Package and the Resulting Product
4,713,785	D. C. Antonelli, P. M. Branscomb, E. C. Broockman, L. D. Dickson, C. P. Koppel, R. Ossowski, and O. L. Stokes, Jr.	Method of Inhibiting Processing of Duplicative Coded Label Signals
4,713,814	G. Andrusch, J. Baisch, H. Barsuhn, F. C. Wernicke, and S. K. Wiedmann	Stability Testing of Semiconductor Memories
4,713,815	D. P. Bryan and J. B. Rowe	Automatic Fault Location System for Electronic Devices

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4,715,430	A. J. Arnold, M. G. Courtney, D. N. Kirby, K. H. Mis, and K. L. Sutton	Environmentally Secure and Thermally Efficient Heat Sink Assembly
4,715,687	D. W. Glass and W. M. Routt, Jr.	Color Variation in a Passively Illuminated Display Using Fluorescent Light Sources
4,715,941	C. R. Jones and R. A. Susko	Surface Modification of Organic Materials to Improve Adhesion
4,716,523	G. S. Burrus, Jr., R. J. Cooper, R. Marr, J. C. Pescatore, and M. A. Marsico	Multiple Port Integrated DMA and Interrupt Controller and Arbitrator
4,716,528	R. A. Crus, D. J. Haderle, and H. W. Herron	Method for Managing Lock Escalation in a Multiprocessing, Multiprogramming Environment
4,716,533	K. Ohmori	Image Translation System
4,716,546	W. F. Beausoleil and D. F. McManigal	Memory Organization for Vertical and Horizontal Vectors in a Raster Scan Display System
4,718,972	S. V. Babu, W. F. Herrmann, J. G. Hoffarth, V. Markovich, and R. T. Wiley	Method of Removing Seed Particles from Circuit Board Substrate Surface
4,719,185	G. R. Goth	Method of Making Shallow Junction Complementary Vertical Bipolar Transistor Pair
4,719,372	B. A. Chappell and S. E. Schuster	Multiplying Interface Circuit for Level Shifting Between FET and TTL Levels
4,719,418	R. C. Flaker and R. J. Houghton	Defect Leakage Screen System
4,719,566	H. H. Kelly	Method for Entrapping Unauthorized Computer Access
4,719,568	F. P. Carrubba, J. Cocke, and N. H. Kreitzer	Hierarchical Memory System Including Separate Cache Memories for Storing Data and Instructions
4,719,571	J. I. Rissanen and M. Wax	Algorithm for Constructing Tree Structured Classifiers
4,719,596	K. Bernstein and A. Furman	Register Providing Simultaneous Reading and Writing to Multiple Ports
4,719,600	D. R. Huffman, S. C. Lewis, and J. E. Rock	Sense Circuit for Multilevel Storage System
4,719,601	K. S. Gray and B. D. Herrman	Column Redundancy for Two Port Random Access Memory
4,719,629	W.-Y. Wang	Dual Fault-Masking Redundancy Logic Circuits
4,720,401	P. S. C. Ho, P. O. Hahn, H. Lefakis, and G. W. Rubloff	Enhanced Adhesion Between Metals and Polymers
4,720,467	R. A. Muggli and T. P. McCaffrey	Method of Forming a Capacitor-Transistor Integrated Circuit
4,720,670	D. H. Boyle	On Chip Performance Predictor Circuit
4,720,705	S. Gupta and B. D. Lucas	Virtual Resolution Displays
4,720,822	A. C. Hansen	High Frequency Signal Measurement Method and Apparatus
4,720,869	A. B. Wadia	Hand Dimension Verification
4,720,923	B. P. Quinton and G. E. Scott	Remote Center Compliance System
4,721,093	M. K. Bullock and F. C. Miller	Line Illumination System
4,721,401	J. W. Malcolm	Printhead Control
4,721,689	P. N. Chaloux, Jr., T. F. Houghton, and R. K. West	Method for Simultaneously Forming an Interconnection Level and Via Studs
4,722,470	A. Johary	Method and Transfer Plate for Applying Solder to Component Leads
4,722,622	E. F. Helinski	Damping Apparatus for a Print Hammer Mechanism
4,722,653	R. C. Williams, B. L. Kendall, F. Deaton, and M. Sedlak	Material Handling for Automated Assembly Facility
4,723,120	J. S. Petty, Jr.	Method and Apparatus for Constructing and Operating Multipoint Communication Networks Utilizing Point-to Point Hardware and Interfaces
4,723,209	I. H. Hernandez, B. A. Barker, and B. H. Machart	Flow Attribute for Text Objects
4,723,210	B. A. Barker and R. A. McCaskill	Superblock Structure in a Multiple in a Data Editor
4,723,211	B. A. Barker, I. H. Hernandez, and R. A. McCaskill	Editing of a Superblock Data Structure
4,723,268	D. C. Newell, K. F. Schroeder, and B. J. Wilkie	Dual Mode Phone Line Interface
4,723,978	D. J. Clodgo, R. A. Previti-Kelly, and E. G. Walton	Method for a Plasma-Treated Polysiloxane Coating
4,724,318	G. K. Binnig	Atomic Force Microscope and Method for Imaging Surfaces with Atomic Resolution
4,724,366	J. J. Neesz	Randomized Motor Drive
4,724,392	U. Bandara, G. Elsner, V. Heinrich, H. Hinkel, A. Lang, E. Prinz, W. Steiner, and W. Zapka	System for Testing Magnetic Head/Disk Interfaces

4,724,499	D. E. Bratvold and R. W. Luoma	Disk Drive Enclosure with an Inclined Parting Surface
4,724,542	J. D. Williford	Automatic Reference Adaptation During Dynamic Signature Verification
4,725,339	P. Bindra, S. L. Levine, and D. N. Light	Method for Monitoring Metal Ion Concentration in Plating Baths
4,725,560	J. R. Abernathy, D. L. Johnson, P.-H. Pan, and C. A. Paquette	Silicon Oxynitride Storage Node Dielectric
4,725,562	B. El-Kareh, R. R. Garnache, and A. K. Ghatalia	Method of Making a Contact to a Trench Isolated Device
4,725,727	C. S. Harder, W. D. Pohl, and E. P. Stoll	Waveguide for an Optical Near-Field Microscope
4,725,743	C. J. Anderson	Two-Stage Digital Logic Circuits Including an Input Switching Stage and an Output Driving Stage Incorporating Gallium Arsenide FET Devices
4,725,815	J. L. Mitchell, K. L. Anderson, and G. Goertzel	Method for Encoding and Decoding a Digital Image
4,725,828	M. F. Cowlshaw	Color Display Apparatus and Method of Coding a Color Image
4,725,829	A. S. Murphy	Automatic Highlighting in a Raster Graphics Display System
4,725,850	D. B. Dove, K. R. Grebe, G. E. Keefe, and E. J. Yarmchuk	Electromagnetic Head with Separate Addressability Resolution and Imaging Resolution Functions
4,725,885	C. A. Gonzales, J. L. Mitchell, and W. B. Pennebaker	Adaptive Graylevel Image Compression System
4,725,892	N. Suzuki, H. Yanagisawa, and T. Yokemura	Method and Apparatus for Enlarging or Reducing an Image on the Basis of Precalculated Reference Pel Contribution Rate and Data and Two-Dimensional Reference Window Shifting Amount Data Stored in a Table Memory
4,725,945	E. P. Kronstadt and S. P. Gandhi	Distributed Cache in Dynamic RAMs
4,725,986	D. J. Kouba	FET Read Only Memory Cell with Word Line Augmented Precharging of the Bit Lines
4,726,018	W. K. Bux, D. M. Dias, and A. Goyal	Method of Providing Priority Access to a Transmission Communication Ring
4,726,022	P. M. Chan and S. C. Chen	Method of Apparatus for Stressing the Data Window in Magnetic Storage Devices
4,726,023	J. L. Carter, L. M. Huisman, and T. W. Williams	Determination of Testability of Combined Logic End Memory by Ignoring Memory
4,726,879	J. A. Bondur, N. J. Giammarco, T. A. Hansen, G. A. Kaplita, and J. S. Lechaton	RIE Process for Etching Silicon Isolation Trenches and Polycides with Vertical Surfaces
4,727,267	K. Bernstein	Clocked Buffer Circuit
4,727,271	A. F. Favata and G. J. Gaudenzi	Apparatus for Increasing the Input Noise Margin of a Gate
4,727,304	P. A. Graham	Phase Sequencer for Stepping Motor
4,727,308	R. J. Huljak, S. F. Newton, and K. A. Wallace	FET Power Converter with Reduced Switching Loss
4,727,313	Z. Barazilai, V. S. Iyengar, B. K. Rosen, and G. M. Silberman	Fault Simulation for Differential Cascode Voltage Switches
4,727,362	D. P. Rackley, J. A. Saenz, and P. S. Yosim	Digital Display System
4,727,477	B. D. Gavril	Logically Transportable Microprocessor Interface Control Unit Permitting Bus Transfers with Different but Compatible Other Microprocessors
4,727,588	A. S. Fox, E. C. Greanias, J. Kim, and C. C. Tappert	System for Automatic Adjustment and Editing of Handwritten Text Image
4,728,621	V. Graf and A. Oosenbrug	Fabricating a Field Effect Transistor Utilizing a Dummy Gate
4,728,623	N. C. Lu and B. J. Machesney	Fabrication Method for Forming a Self-Aligned Contact Window and Connection in an Epitaxial Layer and Device Structures Employing the Method
4,728,624	V. J. Silvestri and P. J. Tsang	Selective Epitaxial Growth Structure and Isolation
4,728,626	K.-N. Tu	Method for Making Planar 3D Heteroepitaxial Semiconductor Structures with Buried Epitaxial Silicides
4,728,751	M. J. Canestaro and W. J. Summa	Flexible Electrical Connection and Method of Making Same
4,728,789	E. C. Broockman, R. T. Cato, and G. J. Laurer	System for Adjusting Holographic Scanner Lockout Voltage
4,728,814	G. E. Smith III	Transistor Inverse Mode Impulse Generator
4,728,945	M. Kojima	Tabulating System
4,729,006	A. J. Dally, S. Ogura, J. Riseman, and N. Rovedo	Sidewall Spacers for CMOS Circuit Stress Relief/Isolation and Method for Making
4,729,091	C. P. Freeman and W. M. Johnson	Directing Storage Requests Prior to Address Comparator Initialization with a Reference Address Range

4,729,096	L. E. Larson	Method and Apparatus for Generating a Translator Program for a Compiler/Interpreter and for Testing the Resulting Translator Program
4,729,115	B. A. Kauffmann and C. H. Lam	Non-Volatile Dynamic Random Access Memory Cell
4,729,533	C. J. Hillary and J. V. Pike	Support Apparatus
4,729,678	D. E. Hays and J. F. Lederer	Method and Apparatus for Producing Characters in Bold Form
4,729,797	H. G. Linde, E. T. Murphy, and D. J. Poley	Process for Removal of Cured Epoxy
4,730,122	J. H. Dreibelbis, R. C. Flaker, and E. L. Hedberg	Power Supply Adapter Systems
4,730,227	D. G. Chong, F. J. Furman, E. L. Mathurin, and T. J. Rajac	Disk File Actuator with Combined Carriage Rail and Isolator Mount for Drive Magnet
4,730,249	J. T. O'Quin II and J. C. O'Quin III	Method to Operate on Large Segments of Data in a Virtual Memory Data Processing System
4,730,251	D. E. Aakre, D. J. Abrahamson, N. C. Berglund, V. A. Bettermann, L. S. Heim, and K. A. Moe	Automatic I/O Address Assignment
4,730,252	K. A. Bradshaw	Document Composition from Parts Inventory
4,730,308	L. G. Freidman and B. T. Hailpern	Interface Between a Computer Bus and a Serial Packet Link
4,730,318	R. Bogholtz, Jr. and L. J. Bosch	Modular Organized Storage Tester
4,730,666	E. B. Flint, K. R. Grebe, P. A. Gruber, and A. R. Zingher	Flexible Finned Heat Exchanger
4,730,823	D. A. Barela, M. A. Beran, L. A. Bustamante, C. M. McDonald, R. E. Reese, and R. D. Shepherd	Adaptive Document Feed Pick Mechanism
4,731,128	F. J. Casullo	Protection of Cooper from Corrosion
4,731,158	J. H. Brannon	High Rate Laser Etching Technique
4,731,606	D. F. Bantz and C. J. Evangelisti	Method for Rapid Windowing of Display Information in Computer Graphics
4,731,609	D. C. Reynolds and M. Wong	Fast Correlation of Markers with Graphic Entities
4,731,643	B. Dunham, J. M. Fitzgerald, and R. R. Williams	Logic-Circuit Layout for Large-Scale Integrated Circuits
4,731,735	K. W. Borgendale, P. S. Cheng, M. D. Flannery, L. K. Peters, and K. A. Zaiken	Multilingual Processing for Screen Image Build and Command Decode in a Word Processor, with Full Command, Message and Help Support
4,731,750	M. A. Hoflich and J. E. Olson	Workstation Resource Sharing
4,731,755	G. C. Bjorklund, M. Y. Chen, and V. B. Jipson	Thermal Design for Reversible Phase Change Optical Storage Media
4,731,773	J. Lewkowicz	Thermally-Induced Recording with Reduced Harmonic Distortion
4,731,784	H. J. Keller, J. R. Mueller, E. H. Rothausen, and E. A. Zurfluh	Communication System Comprising Overlaid Multiple-Access Transmission Networks
4,731,829	Y. A. Bonnet and C. Jacquart	Circuit for Detecting Current Variations
4,731,842	P. R. Smith	Security Module for an Electronic Funds Transfer System
4,731,857	C. C. Tappert	Recognition System for Run-On Handwritten Characters
4,731,860	F. M. Wahl	Method for Identifying Three-Dimensional Objects Using Two-Dimensional Images
4,732,501	T. V. Angst and J. G. Goodwin	Method and Apparatus for Loading of a Web into a Printer
4,732,636	K. J. Varker	Method for Producing a Prepreg Having Improved Dimensional Stability
4,732,646	G. Elsner, J. Greschner, and H. Hinkel	Method of Forming Identically Positioned Alignment Marks on Opposite Sides of a Semiconductor Wafer
4,732,801	R. V. Joshi	Graded Oxide/Nitride Via Structure and Method of Fabrication Therefor
4,733,177	J. P. Pawletko	High Resolution High Output Magneto Resistive Transducer for Determining Static and Dynamic Position
4,733,268	G. S. Beardsley and M. H. Lee	Voltage Control Bar for Electrophotography
4,733,282	L. L. Chang and L. Esaki	One-Dimensional Quantum Pipeline Type Carrier Path Semiconductor Devices
4,734,046	M. F. McAllister and A. Mack	Coaxial Converter with Resilient Terminal
4,734,157	S. R. Carbaugh, H. Y. Ng, M. S. Polavarapu, and D. Stanasolovich	Selective and Anisotropic Dry Etching
4,734,175	W. D. Bissinger	Process for Regenerating an Electroless Copper Plating Bath
4,734,676	S. Huon and V. Spagnol	Method and Device for Detecting a Particular Bit Pattern in a Serial Train of Bits
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4,734,803	H. S. Nishihira	Magnetic Head Air Bearing Slider
4,734,900	N. R. Davie	Restoring and Clocking Pulse Width Modulated Data